

Runtime Verification

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Where am I Sitting?

- NASA
 - JPL, California, Los Angeles: unmanned deep space
 - Houston, Texas: manned missions
 - Kennedy, Florida: launches
 -
 - NASA Ames, California, Mountain View:

Computer Science:

- Computational Sciences Division: 300 researchers
 - Automated Software Engineering Group
 - Verification and Testing: 10 people
 - Program Synthesis: 10 people
 - Planning and Scheduling
 - ...
- Super computing
- ...

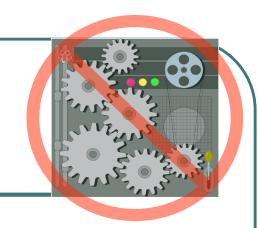


The Team

- Cyrille Artho (ETH, Zurich, CH)
- Howard Barringer (U. Manchester, UK)
- Saddek Bensalem (Verimag, Grenoble, F)
- Allen Goldberg(KT/NASA Ames, USA)
- Klaus Havelund (KT/NASA Ames, USA)
- Koushik Sen (Univ. Illinois, USA)

M I S S I O N : W A R S 2 0 1 6

NASA Increasingly Relies on Software



- Systems must support remote Not only HW exploration
- Systems must be more autonomous
- Systems must do more complex tasks

When people think of space, they think of rocket plumes and the Space Shuttle, but the future of space is information technology...

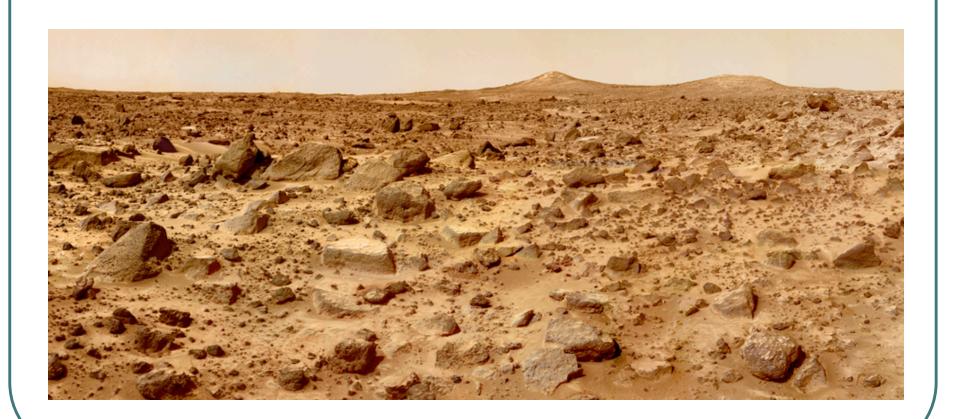
Daniel S. Goldin,

Previous NASA Administrator





NASA gets Excited when it goes well









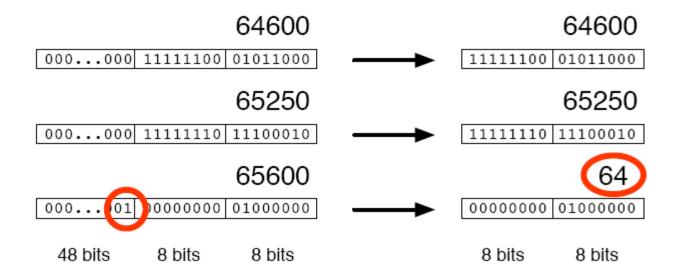


Ariane 5, 1996 - Lost









Data conversion of a too large number. 64 bit floating point number relating to the horizontal velocity of the rocket with respect to the platform was converted to a 16 bit signed integer. Number was larger than 65,536.

Due to higher horizontal velocity than in Ariane 4.

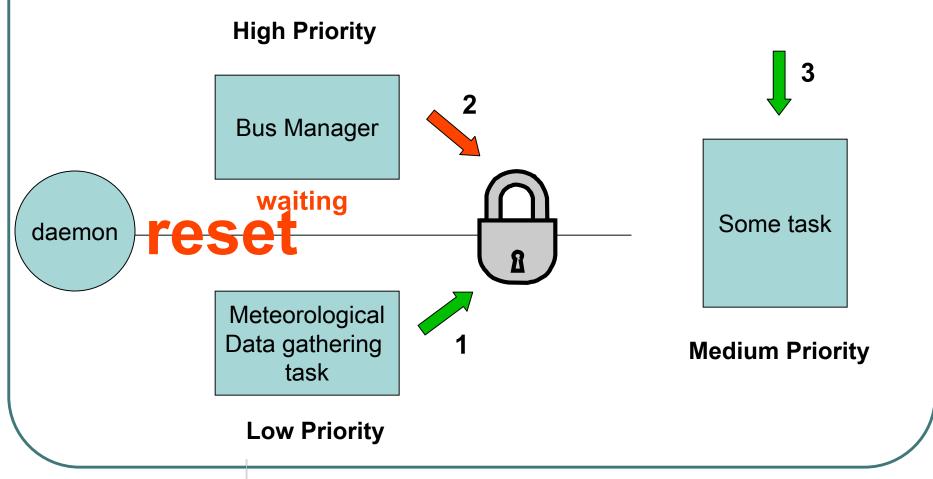


Mars PathFinder, 1997



Mars PathFinder: Priority Inversion Problem

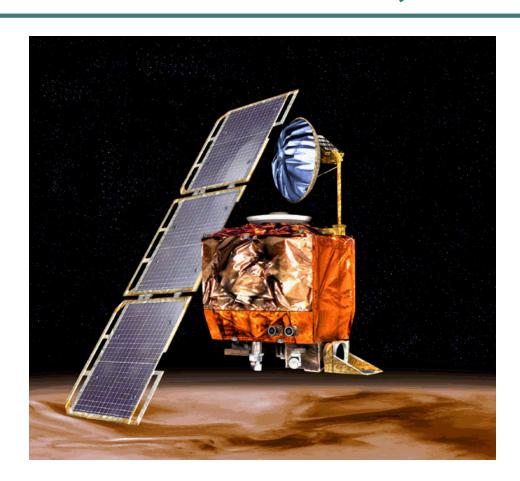








Mars Climate Orbiter, 1999



Mars Climate Orbiter: Unit error



Pounds, inches, ...

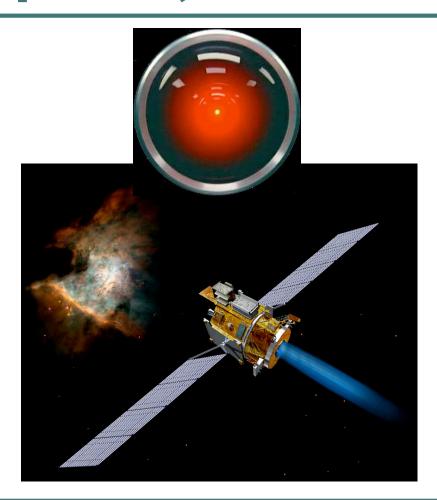


Kilos, centimeters

"We had planned to approach the planet at an altitude of about 150 kilometers (93 miles). We thought we were doing that, but upon review of the last six to eight hours of data leading up to arrival, we saw indications that the actual approach altitude had been much lower. It appears that the actual altitude was about 60 kilometers (37 miles)".



Deep Space 1, 1999





Deep-Space 1

```
while(true) {
   action();
   if(!newEvents())
       wait();
   handleEvents();
}
```

Deep-Space1: We found error before flight



- Using the SPIN model checker
- In spacecraft operating system
- Code was corrected, but error later reintroduced in a different sub-system
- Error was located after 5 hours
- Was not fixed since modifying code could cause new errors, and ...
 - it was not likely to re-occur
- Shows how hard these errors are to find.



Mars Polar Lander, 1999



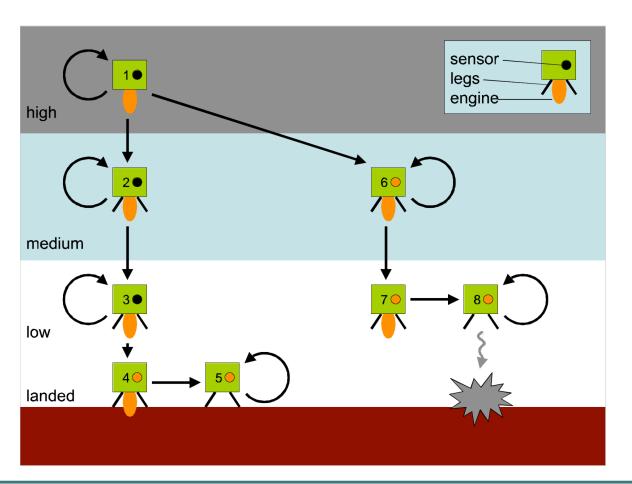
Mars Polar Lander: Landing Sensor activated too early



- Normally, the shake of a touch down would signal that engines should be shut down.
- However, shake of legs opening could cause the same effect in some cases. It was known.
- System was designed to ignore such shakes above 40 feet where legs were to open.
- System above 40 feet correctly ignored landed-flag, but flag was not reset to false, and triggered engine shut-off as soon as 40 feet were reached.

Mars Polar Lander: Imagined Scenario





40 feet ~ 13 meters

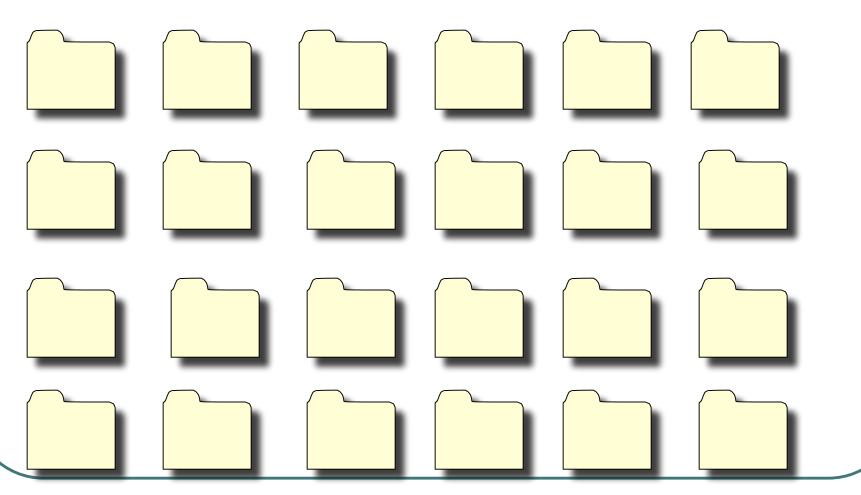


Spirit Mars Rover, 2004



Spirit Mars Rover: Too many files allocated







Some Observations

- Software applications for space missions have grown from a few thousands of lines of code in the late seventies to <u>hundreds of thousands of lines</u> of code for today's missions.
- At the current rate, the code size for controlling spacecraft <u>doubles</u> in size every four years.
- Software should be expected to contain between 1 and 10 defects per 1,000 lines of code excluding comments and blank lines.
- We are talking about hundreds of errors on current missions, and more to come.



Also Complexity grows

But, software is not just rapidly growing in size; it is also rapidly growing in complexity. Virtually all current

missions use multi-threaded software designs: running up to 50 threads executing concurrently and requiring synchronization of potentially conflicting tasks.



What can We Do?

prevent, detect, and control



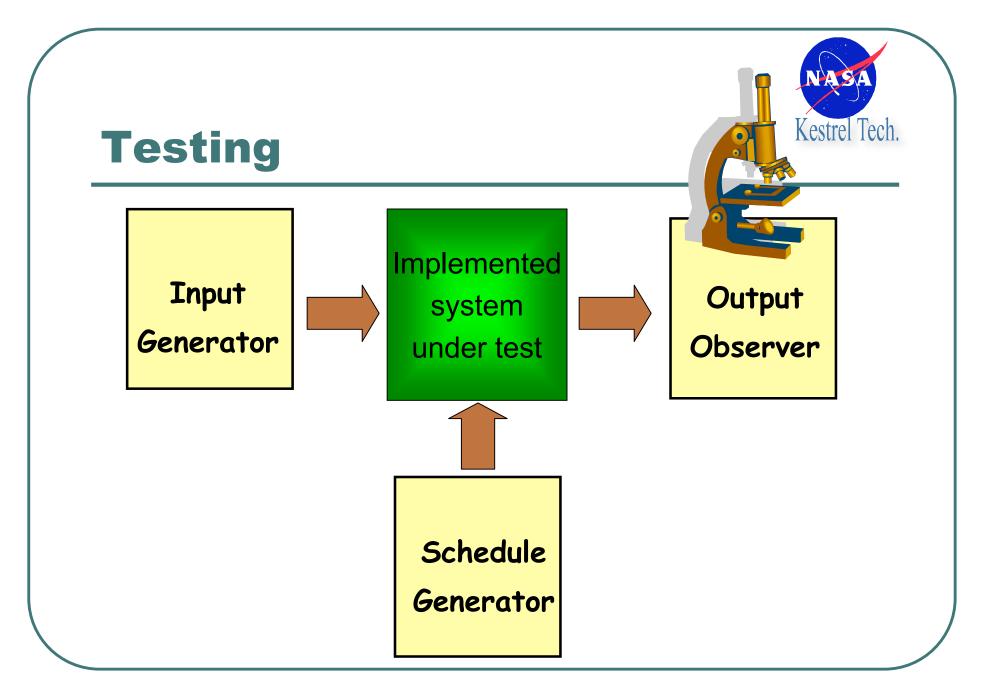
What can We Do?

Solid formal designs
Safe programming languages

prevent, detect, and control

Test
Analysis

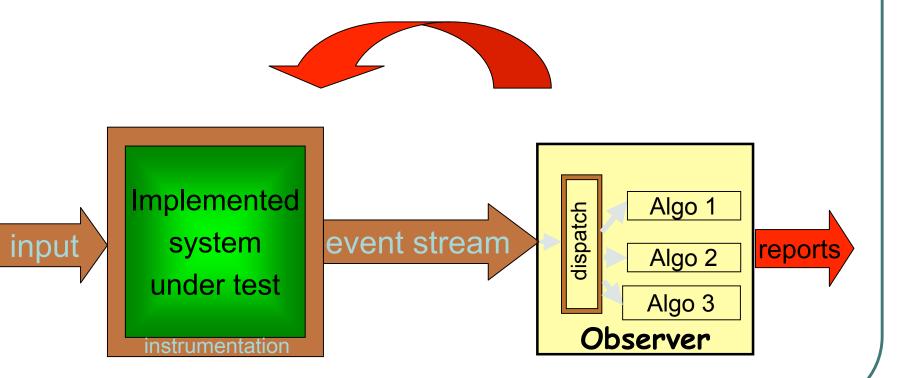
Fault containment





Runtime Verification

Fault containment





What is An Event Stream?

```
while(true) {
   if(x>0)lock(L);
   x = shared;
   shared = f(x);
   release(L);
}
```



What is An Event Stream?

```
Trace:
while(true){
                       Instrument program.
  if(x>0){
                       For example using
                       Aspect Oriented Programming , L)
     lock(L);
     logLock(t,L);
                                     lock(t2,L)
                                     x=12
                                                     monitor
                         execute
                                     shared=24
  x = shared;
                                     release(t2,L)
                                     lock(t2,L)
                logWr('x',x);
                                     x = 2.4
  shared = f(x);
                                     shared=48
  logWr('shared', shared);
                                     release(t2,L)
                                     lock(t2,L)
  release(L);
                                     x = 50
  logRelease(t,L);
                                     shared=100
                                     release(t2,L)
                                     lock(t2,L)
                                                   29
                                     x = 100
```

Runtime Verification Algorithms



- Requirement monitoring
 - The Eagle Temporal logic
- Concurrency Analysis
 - Deadlock analysis
 - Data race analysis
 - Low level data races
 - High level data races
 - Data flow races

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Allows easy specification of properties of an execution/log file:

Safety properties:

"x is always positive".

• Liveness properties:

"a turn signal is followed by a turn within 10 seconds".

Past and future:

"When a turn occurs, a turn command has been emitted before".

Requirement Specification: So many logics, notations, languages ...



General specification language suitable for monitoring? Supports many styles:

- state machines
- temporal logic: 0x>0 (eventually x>0)
 future+past
- regular expressions: login+ use* logout
- real-time properties: ◊[10]x>0
- properties about data values over time:

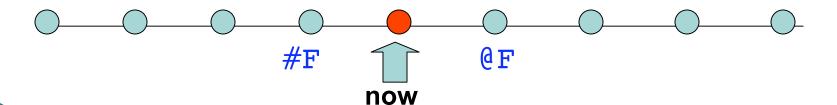
 \Box (login(u) -> \Diamond logout(u))



Eagle's Core Concepts

- Three temporal connectives:
 - Next: @F
 - Previous: #F
 - Concatenation: F1; F2
- Recursive parameterized rules over trace

```
Always(Term t) = t / \ @Always(t).
```





Basic LTL Combinators

```
library
// Future time combinators
\max Always(Term t) = t / \ @ Always(t).
min Until(Term t1,Term t2) =
     t2 \/ (t1 /\ @ Until(t1,t2)) .
// Past time combinators
max Sofar(Term t) = t /  # Sofar(t) .
min Previously(Term t) = t \/ # Previously(t) .
min Since(Term t1,Term t2) =
     t2 \/ (t1 /\ # Since(t1,t2)) .
```



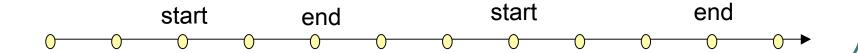
Example

Property:

Every start is followed by an end, and vice versa.

```
mon M1 = Always(start() -> Eventually(end())
```

mon M2 = Always(end() -> Previously(start())





Data Bindings y has had that value.

Property:
when x>0 then
y has had that value

mon
$$M = Always(x>0 -> R(x))$$

min $R(int k) = Previously(y==k))$

$$y==k$$
 $y==x$
 $x>0$

Real-Time is Just Data

Property:





```
min WithinAbs(float t1, float t2, Term F) =
                                                      library
      clock <= t2 /\
       (F - t1 <= clock) /\
       ( ~ F @ EventuallyAt(t1, t2,F)) .
min Within(<u>float</u> t1, <u>float</u> t2, <u>Term</u> F) =
      WithinAbs(t1+clock, t2+clock, F).
mon M = Always(start() => Within(1,4,end()))
           [1 .. 4]
                                      [1 .. 4]
                                start
                                                end
       start
                 end
```



Grammars

Property:

Locks are acquired and released nested.

lock lock release lock release release



lock lock lock lock lock release



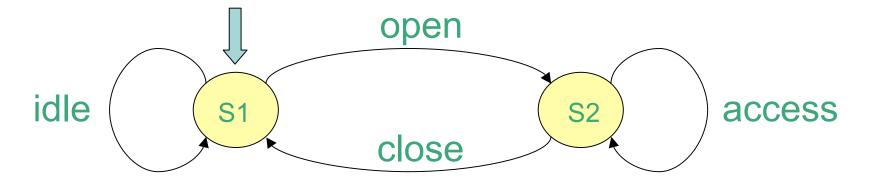
```
mon M = Match(lock(),release())
min Match (Term 1, Term r) =
   Empty() \/
   (1;Match(l,r);r;Match(l,r))
```

Property:



File accesses are always enclosed by open and close operations.

State Machines operations.



```
max S1() = open -> @ S2()
/\ idle -> @ S1()
```

$$mon M = S1()$$



Syntax

```
S ::= \operatorname{dec} D \operatorname{obs} O
D ::= R^*
O ::= M^*
R ::= \{ \max \mid \min \} N(T_1 x_1, \dots, T_n x_n) = F
M ::= N = F
T ::= \operatorname{Form} | \text{java primitive type}
F ::= \text{java expression} | \operatorname{True} | \operatorname{False} | \neg F | F_1 \land F_2 | F_1 \lor F_2 | F_1 \to F_2 |
\bigcirc F | \bigcirc F | F_1 \cdot F_2 | N(F_1, \dots, F_n)
```

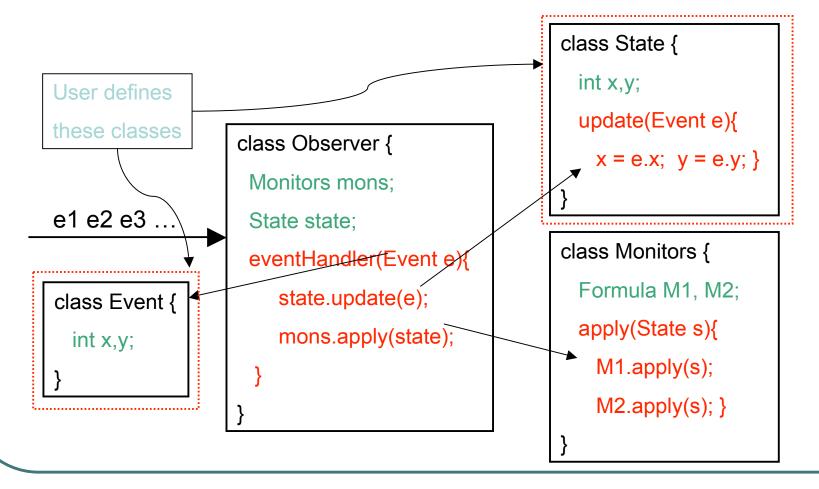


Semantics

```
\begin{array}{ll} \sigma, l \models_D \exp & \text{iff } 1 \leq l \leq |\sigma| \text{ and } evaluate(exp)(\sigma(l)) == true \\ \sigma, l \models_D \underline{\text{true}} \\ \sigma, l \models_D \underline{\text{false}} \\ \sigma, l \models_D F & \text{iff } \sigma, l \models_D F \\ \sigma, l \models_D F_1 \land F_2 & \text{iff } \sigma, l \models_D F_1 \text{ and } \sigma, l \models_D F_2 \\ \sigma, l \models_D F_1 \lor F_2 & \text{iff } \sigma, l \models_D F_1 \text{ or } \sigma, l \models_D F_2 \\ \sigma, l \models_D F_1 \to F_2 & \text{iff } \sigma, l \models_D F_1 \text{ implies } \sigma, l \models_D F_2 \\ \sigma, l \models_D \bigcirc F & \text{iff } l \leq |\sigma| \text{ and } \sigma, l+1 \models_D F \\ \sigma, l \models_D \bigcap F & \text{iff } 1 \leq l \text{ and } \sigma, l+1 \models_D F \\ \sigma, l \models_D F_1 \cdot F_2 & \text{iff } \exists f \text{ s.t. } l \leq f \leq |\sigma| + 1 \text{ and } \sigma^{[1,j-1]}, l \models_D F_1 \text{ and } \sigma^{[j,|\sigma|]}, 1 \models_D F_2 \\ \hline \sigma, l \models_D N(F_1, \dots, F_m) & \text{iff } \begin{cases} \text{if } 1 \leq l \leq |\sigma| \text{ then:} \\ \sigma, l \models_D F[x_1 \mapsto F_1, \dots, x_m \mapsto F_m] \\ \text{where } (N(T_1 x_1, \dots, T_m x_m) = F) \in D \\ \text{otherwise, if } l = 0 \text{ or } l = |\sigma| + 1 \text{ then:} \\ \text{rule } N \text{ is defined as } \underline{\text{max}} \text{ in } D \end{cases} \end{array}
```



How does it Work?





Implementation Ideas

- For each new event, the monitored formula is evaluated, resulting in either:
 - True it is satisfied
 - False it is violated
 - A non-reducable formula that the rest of the trace must satisfy.
- Then all formulas appearing after # (previous state) are evaluated and remembered for the next state.



Example Evaluation

mon
$$M = Always(y>0 \rightarrow R(y))$$

min $R(int k) = \# k==x /\ @ k==z$

$$y>0$$
 $k:=y$
 $z==k$

Always($y>0 \rightarrow R(y)$)

$$R(int k) = \# k==x / \ @ k==z$$



Evaluating

state	formula		k==x
x=77	Always(y>0	->R(y))	k==77
y=0 =			
z=18			
x=28	Always(y>0	->R(y))	k==28
y=77 ==	/\ 77==z		
z=2			
x=1	Always(y>0	->R(y))	k==1
y=0			
z=77			





```
Observer Locks{
   var Thread t, FileSystem fs, int 1;

   mon M = Always(
        [t?:fs?.lock(1?)]
        Until(
        ~<t:fs.lock(1)>true,
        <t:fs.release(1)>true
   )
}
```

But Properties are Hard to Formulate



To quote quite excellent NASA software engineer when asked what properties his system would have to satisfy:

"I have absolutely no idea what properties this system should satisfy".



K9 Planetary Rover Executive



- Executive receives plans from a planner for direct execution
- Plan is a hierarchical structure of actions
- Multi-threaded system (35K lines of C++)

Example of Plan



```
[1,5] [1,30] [10,16]

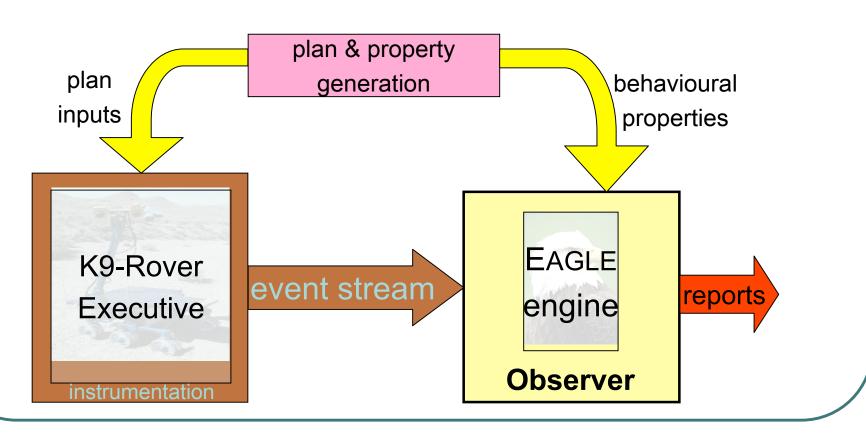
drive1 drive2

20 fail
```

With Willem Visser and Corina Pasareanu

Running X9





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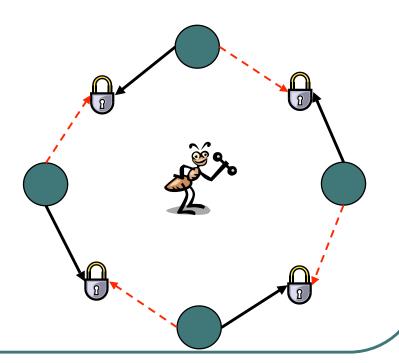
More Power - More Problems

- Multi-threaded programs may execute differently from one run to another due to the apparent randomness in the way threads are scheduled.
- Typically, testing cannot explore all schedules, so some bad schedules may never be discovered.



Cyclic Deadlocks

A resource deadlock can occur when two or more threads block each other in a cycle while trying to access synchronization locks (held by other threads) needed to continue their activities.





Cyclic Deadlocks

Deadlock: if T1 takes R1 and then T2 takes R2

```
T1:

| lock(R1); | lock(R2); | lock(R1); | release(R1); | release(R1); | release(R2); | release(
```





Deadlock: if T1 takes R1 and then T2 takes R2

```
T1:

| lock(R1); | lock(R2); | lock(R1); | lock(R1); | lock(R1); | release(R1); | release(R1); | release(R2); | release(R2); | R1 | R2
```



A Miracle?

- Deadlock potential detected even though a deadlock did not occur in that run
- Reason: we are checking a stronger property:
 - Weaker property: deadlock freedom
 - Stronger property: cycle freedom



Singular cycle



```
Guarded cycle
                                Deadlock cycle!
                       T2:
T1:
                                              T3:
sync(G){
                       sync(G){
                                              sync(L1){
  sync(L1){
                          sync(L2){
                                                sync(L2){}
     sync(L2){}
                            sync(L1){}
T3 = new T3();
j3.start();
J3.join();
                    Thread segmented cycle
sync(L2) {←
                                                  4 deadlock potentials
  sync(L1){}
                                                  Only one is real
```



Execution Trace

Trace

```
T1:
                     T2:
                                         T3:
sync(G){
                     sync(G){
                                        sync(L1){
                     sync(L2){
  sync(L1){
                                           sync(L2){}
    sync(L2){}
                         sync(L1){} }
};
T3 = new T3();
j3.start();
                       Event format:
J3.join();
                   I(<thread>,<lock>) - lock
sync(L2){
  sync(L1){}
                   u(<thread>,<lock>) - unlock
                   s(<thread>,<thread>) - start
                  j(<thread>,<thread>) - join
```

```
1(T1,G)
1(T1,L1)
1(T1,L2)
u(T1,L2)
u(T1,L1)
s(T1,T3)
1(T2,G)
1(T2,L2)
1(T2,L1)
u(T2,L1)
u(T2,L2)
u(T2,G)
j(T1,T3)
1(T1,L2)
1(T1,L1)
u(T1,L1)
u(T1,L2)
```

NASA Kestrel Tech

new T1().start();

M:

Full Algorithm

3. Segments: must be parallel

```
•new T2().start();
        T1:
                                 T2:
                                                         T3:
        sync(G){
                                 sync(G){
                                                         sync(L1){
           sync(L1){
                                   sync(L2){
                                                           sync(L2){}
             sync(L2){}
                                      sync(L1){}
        };
                                          One potential left, the real deadlock!
        T3 = new T3();
                              T3,{},(6,6)
     • j3.start();
     • J3.join();
                              sync(L2){
           sync(L1){} L1
                                         L2
                                             T1
Valid Cycles:
                              T2,{G},(4,4)
                                             T2
1. Threads: must differ
                              T1,{},(7,7)
2. Guard sets: must not overlap
                                             T3
```

Static Code Analysis Fails On Some Examples



Static analysis cannot find this problem due to the dynamic creation of forks and the '%' operator (experiment with JLint).

Model checking works for N=20, but if program is deadlock free (introducing gate lock) N=3 is max using 3 minutes (JPF).

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Data Races

Standard definition:

A data race occurs when two concurrent threads access a shared variable and when at least one access is a write, and the threads use no explicit mechanism to prevent the accesses from being simultaneous.



A Classic Java Example

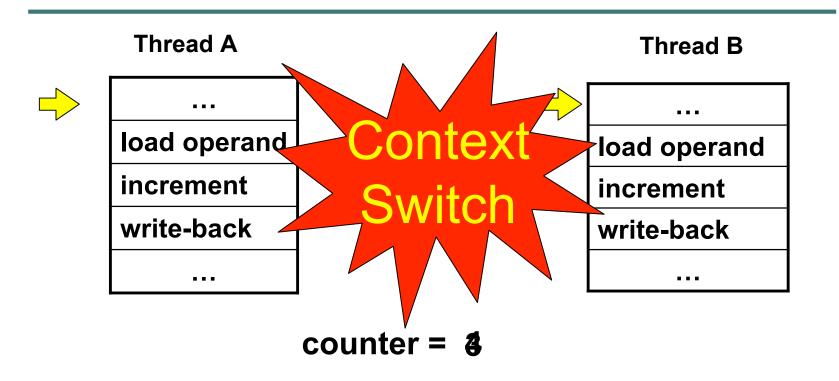
Let's consider the function increase(), which is a part of a class that acts as a counter

```
public void
increase()
{
     counter++;
}
```

Although written as a single "increment" operation, the "++" operator is actually mapped into three JVM instructions [load operand, increment, write-back]

Example – Continued





We shall refer to this traditional notion of data race as a *low-level data race*, since it focuses on a single variable



Low-Level Data Races

- The standard way to avoid low-level data races on a variable is to protect the variable with a lock: all accessing threads must acquire this lock before accessing the variable, and release it again after.
- There exist several algorithms for analyzing multi-threaded programs for low-level data races.
- We will mention the Eraser algorithm here (Savage et al 97).



Simple Algorithm

```
synchronized(R1) {
    sum = sum + 100;
}
sum = sum + 100;
}
sum = sum + 50;
}
```

Initially: Lockset = {}

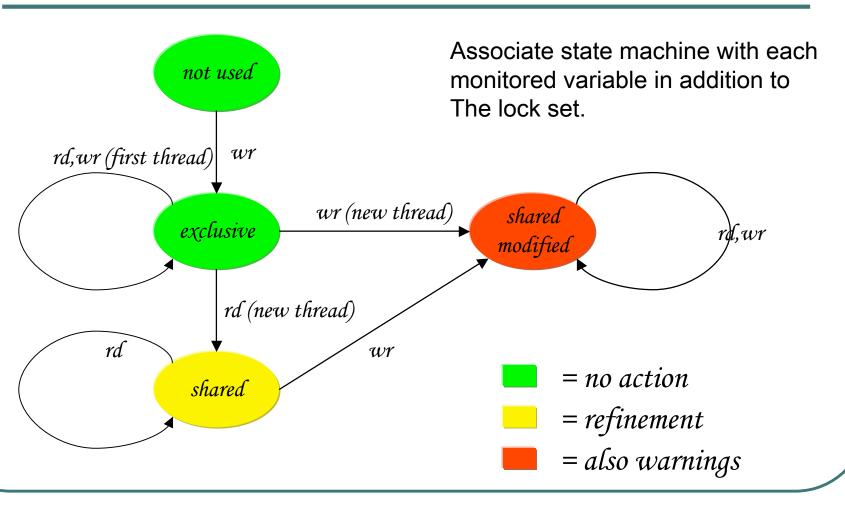
T1 executes: Lockset = $\{R1\}$

T2 executes: $Lockset = Lockset \cap \{R2\} = \{\}$









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Data Race

Pair of coordinates *x* and *y*.

Two threads.

Problem: thread order non-deterministic.

Data corruption possible!

Lock protection needed.



High-Level Data Race

5,8

```
void reset() {
    synchronized(this){
    c.x = 0;
    } 0,8
    synchronized(this){
    c.y = 0;
    }
}
```

Result is neither a swap or a reset

All field accesses synchronized: Eraser reports no errors.

No classical data race for these threads, but clearly undesired behavior!

Problem: **swap** may run while **reset** is in progress!



The Problem

- The reset method releases its lock in between setting x and then setting y.
- This gives the swap method the chance to interleave the two partial resets.
- The swap method "has it right": it holds its lock during operation on x and y.
- This difference in views can be detected dynamically.
- Depends on at least one thread getting it right.



The Solution

- This difference in views can be detected dynamically.
- Essentially, this approach tries to infer what the developer intended when writing the multi-threaded code, by discovering view inconsistencies.
- Depends on at least one thread getting it right.

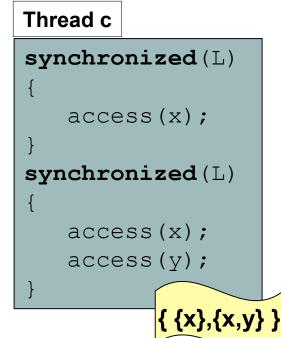
Views express per thread what fields are guarded by a lock. Some Examples of Views:



```
Thread a
synchronized(L)
   access(x);
   access(y);
            { {x,y} }
Thread d
synchronized(L)
   access(x);
               { {x} }
```

```
Thread b

synchronized(L)
{
   access(x);
}
synchronized(L)
{
   access(y);
}
```



Consistent:	a and c, a and d
Inconsistent:	a and b



The Algorithm

- For each thread, for each lock, identify all fields covered by that lock (views).
- 2) For each thread, find the views that have no other view that contains them (maximal views).
- 3) For each pair of threads t1 and t2: find the intersection between t1's maximal view and the views of t2.
- 4) Verify that those intersections form a chain. That is:

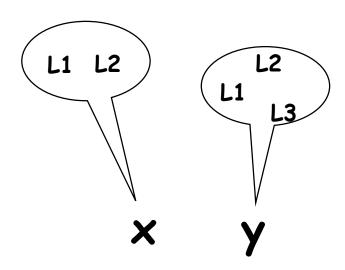
$$s1 \subseteq s2 \subseteq s3 \subseteq \cdots$$

Low-Level versus High-Level Data races



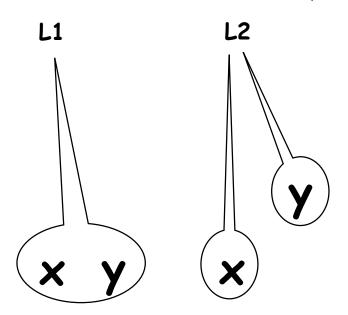
Low-Level

For each variable: a lock set



High-Level

For each lock: a variable set (several)



Applying Algorithm to Example

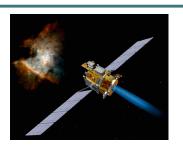


```
void swap() {
  synchronized(this){
    1x = c.x;
    ly = c.y;
  synchronized(this){
    c.x = 1y;
    c.y = 1x;
                  x, y
              maximal of swap
```

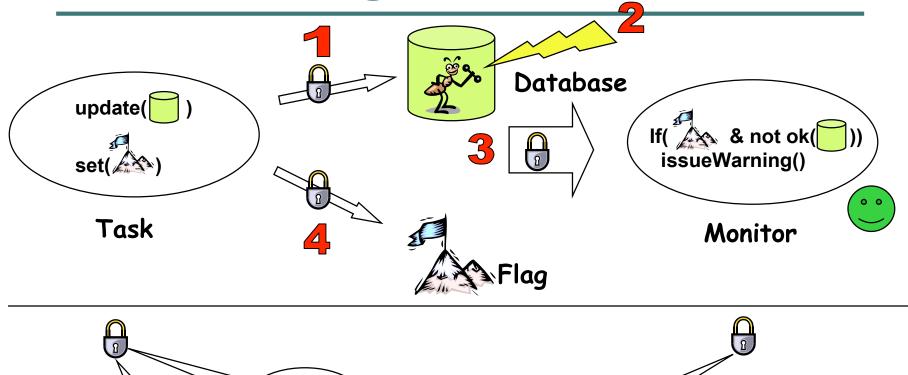
```
void reset() {
    synchronized(this) {
        c.x = 0;
    }
    synchronized(this) {
        .y = 0;
    }
}
Overlaps are:
{x} and {y}.

X
y
{y} \neq {x}
```

HL Data Race in Remote Agent





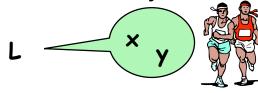


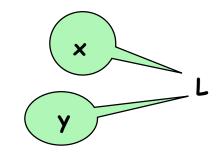
Neither Sound Nor Complete



False positive when one thread uses coarser locking that

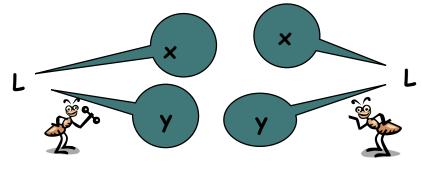
required due to efficiency.



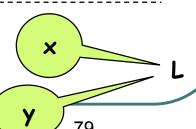


False negatives when:

- □ All threads use the same locking
- Random execution trace does not expose problem







So, what is it good for?



- Much higher chance of detecting an error than if one relies on actually executing the particular interleaving that leads to an error, without requiring much computational resources.
- Developers seem to follow the guideline of view consistency to a surprisingly large extent.

Runtime Verification Algorithms



- Requirement monitoring
 - The Eagle Temporal logic
- Concurrency Analysis
 - Deadlock analysis
 - Data race analysis
 - Low level data races
 - High level data races
 - Data flow races





```
void swap() {
    synchronized(this) {
        lx = c.x;
        ly = c.y;
    }
    synchronized(this) {
        c.x = 0;
    }
    synchronized(this) {
        c.y = 0;
        c.y = 1x;
    }
}
```

Problem: swap may run while reset is in progress!





5,8

```
void swap() {
    synchronized(this) {
        lx = c.x;
        ly = c.y;
        synchronized(this) {
              c.x = 0;
              c.y = 0;
        } 5,8
        synchronized(this) {
              c.x = ly;
              c.y = lx;
        }
        8,5
        Reset invoked after swap, but has no effect
```

Problem:

- reset may run while swap is in progress!
- swap then continues operating on outdated values

The Problem: Data Flow Across Synchronized Blocks



Shared data "escape" beyond first synchronized block!

Algorithm checks whether shared data escape synchronized blocks.



Algorithm

- Enumerate synchronized blocks.
- Mark values as shared or unshared.
- Mark local variables with
 - the identity of synchronization block where defined.
 - Whether they contain a shared variable.
- For each use of a local variable, check:
 - block where used = block where defined.



Determining Sharedness

If instruction **creates** stack elements (getfield, method call)

- if inside a synchronized block: stack elements generated are marked as shared
- else: stack elements generated are marked as local

Determining Sharedness of Return Values of Methods



```
synchronized(this) {
    lx = c.getX();

synchronized int getX() {
    return x;

}

synchronized int getX() {
    return x;

}

Method call outside synchronization:
    callee uses synchronization:
    return value is shared

Method call outside synchronization:
    return value is shared

Method call outside synchronization:
    return value is shared

Method call outside synchronization:
    no synchronization in callee:
    return value is local
```

lx = c.qetX();



Workshop

Fifth International Workshop on

Runtime Verification

RV'05

CAV'05
June, 2005
Edinburgh
Scottland